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[File 348] EUROPEAN PATENTS 1978-200850

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[File 349] PCT FULLTEXT 1979-2008/UB=20081211|UT=20081204

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; d s

Set Items Description

S1 314 S (KERNEL(3N)(EMULAT? OR SIMULATOR? ? OR VIRTUAL))

S2 99839 S ("NON" (NATIVE OR NONNATIVE OR NATIVE)

S3 1244 S (CONVERT? OR CONVERSION? ? OR CHANGE?? OR CHANGING OR TRANSLAT? OR CHANG? OR ALTER? OR ALTERATION? ? OR MODIF? OR TRANSFORM? OR REPLAC??? OR SUBSTITUT???)/(2N)KERNEL? ?

S4 8 S S3(20N)S2

S5 2 S S4(20N)S1

S6 34 S S1(100N)S3

S7 9 S S6 AND PY=1963:2001

?

Subject summary

? 1/3.k/all

7/3K/1 (Item 1 from file: 348) [Links](#)Fulltext available through: [Order File History](#)

EUROPEAN PATENTS

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01387044

SECURE DOMAIN NAME REGISTRATION

SICHERERE REGISTRIERUNG VON DOMANENNAMEN

ENREGISTREMENT SECURISE DES NOMS DE DOMAINES

Patent Assignee:

- SCIENCE APPLICATIONS INTERNATIONAL CORPORATION: (755063)

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(Proprietor designated states: all)

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26203 Ocala Circle; South Riding, VA 20152; (US)

Legal Representative:

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Venner Shipley LLP 20 Little Britain; London EC1A 7DH; (GB)

	Country	Number	Kind	Date	
Patent	EP	1284079	A2	20030219	(Basic)
	EP	1284079	B1	20060118	
	WO	2001092997		20011206	
Application	EP	2001932628		20010425	
	WO	2001US13260		20010425	
Priorities	US	558210		20000426	

Designated States:

CH; DE; GB; IT; LI; NL;

Extended Designated States:

AL; LT; LV; MK; RO; SI;

Related Divisions: Patent (Application): EP 1542429 (EP 2005102086)

International Patent Class (V7): H04L-029/12; H04L-029/06; G06F-017/60

IPC	Level	Value	Position	Status	Version	Action	Source	Office
H04L-0029/12	A	I	F	B	20060101	20051125	H	EP
H04L-0029/06	A	I	L	B	20060101	20051125	H	EP

NOTE: No A-document published by EPO

Type	Pub. Date	Kind	Text
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Publication: English

Procedural: English

Application: English

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200603	310
CLAIMS B	(German)	200603	309
CLAIMS B	(French)	200603	405
SPEC B	(English)	200603	30659
Total Word Count (Document A) 0			
Total Word Count (Document B) 31683			
Total Word Count (All Documents) 31683			

Specification: ...server side replies by sending an information packet to the client computer that has been modified at the kernel layer to containing virtual private connection information in the payload portion of the reply information packet. Preferably, the information...

7/3K/2 (Item 2 from file: 348) [Links](#)Fulltext available through: [Order File History](#)

EUROPEAN PATENTS

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00573739

Dynamically configurable kernel

Dynamisch konfigurierbares Kernsystem

Noyau configurable de façon dynamique

Patent Assignee:

● SUN MICROSYSTEMS, INC.; (1392730)

2550 Garcia Avenue; Mountain View, CA 94043; (US)

(Proprietor designated states: all)

Inventor:

● Allen, Tom

249 Lakeshore Drive; Marlborough, Massachusetts 01752; (US)

● Provino, Joseph E.

29 Wendell Street; Cambridge, Massachusetts 02138; (US)

● Pittore, William F.

6 Blodgett Road; Lexington, Massachusetts 02173; (US)

Legal Representative:

● Wombwell, Francis (46021)

Potts, Kerr & Co. 15, Hamilton Square; Birkenhead Merseyside L41 6BR; (GB)

	Country	Number	Kind	Date	
Patent	EP	573190	A2	19931208	(Basic)
	EP	573190	A3	19940202	
	EP	573190	B1	20010905	
Application	EP	93303987		19930521	
Priorities	US	893337		19920603	

Designated States:

DE; FR; IT; NL; SE;

International Patent Class (V7): G06F-009/445Abstract Word Count: 149

NOTE: 1

NOTE: Figure number on first page: 1

Type	Pub. Date	Kind	Text
Publication: English			
Procedural: English			
Application: English			
Available Text	Language	Update	Word Count
CLAIMS A	(English)		1392
SPEC A	(English)		6657
CLAIMS B	(English)	200136	1344
CLAIMS B	(German)	200136	1317
CLAIMS B	(French)	200136	1561
SPEC B	(English)	200136	6855
Total Word Count (Document A) 8049			
Total Word Count (Document B) 11077			
Total Word Count (All Documents) 19126			

Specification: ...and it is typically beyond the skill of a typical user.

In the present invention, virtual kernel functionality is provided. This is achieved by providing modules in the kernel memory on demand...i.e., (rebuild the kernel) if any of the modules to be loaded into the kernel are changed.

Although it is well known in the art that drivers can be loaded by a...OF THE INVENTION

The system of the present invention sets forth a system for providing virtual kernel functionality. Each module is separately compiled into an executable image. These modules are placed in...

Specification: ...an article entitled "Dynamic Link Libraries for DOS" by Gary Syck.

In the present invention, virtual kernel functionality is provided. This is achieved by providing modules in the kernel memory on demand...i.e., (rebuild the kernel) if any of the modules to be loaded into the kernel are changed.

Although it is well known in the art that drivers can be loaded by a...to maintain the kernel.

SUMMARY OF THE INVENTION

A system and a method for providing virtual kernel functionality according to the present invention are set out in claims 7 and 1, respectively...

7/3K/3 (Item 3 from file: 348) [Links](#)Fulltext available through: [Order File History](#)

EUROPEAN PATENTS

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00420693

Parallel processing system

Paralleles Verarbeitungssystem

Système de traitement parallèle

Patent Assignee:

- MYRIAS RESEARCH CORPORATION; (1278960)

900 Park Plaza, 10611 - 96 Avenue; Edmonton, Alberta, T5K 2P7; (CA)

(Proprietor designated states: all)

Inventor:

- Broughton, Colin G.

11502 - 77th Avenue; Edmonton, Alberta T6G 0M1; (CA)

- Savage, James R.

P.O. Box 4942, County of Parkland; Edmonton, Alberta T6E 5G8; (CA)

Legal Representative:

- Sparing Rohl Henseler Patentanwälte (100362)

Postfach 14 04 43; 40074 Düsseldorf; (DE)

	Country	Number	Kind	Date	
Patent	EP	420142	A2	19910403	(Basic)
	EP	420142	A3	19930324	
	EP	420142	B1	20000308	
Application	EP	90118379		19900925	
Priorities	US	414990		19890929	

Designated States:

CH; DE; ES; FR; GB; IT; LI;

International Patent Class (V7): G06F-009/46 Abstract Word Count: 112

Type	Pub. Date	Kind	Text
Publication:	English		
Procedural:	English		
Application:	English		
Available Text	Language	Update	Word Count
CLAIMS B	(English)	200010	820
CLAIMS B	(German)	200010	753
CLAIMS B	(French)	200010	818
SPEC B	(English)	200010	41844
Total Word Count (Document A) 0			
Total Word Count (Document B) 44235			
Total Word Count (All Documents) 44235			

7/3K/4 (Item 1 from file: 349) [Links](#)Fulltext available through: [Order File History](#)

PCT FULLTEXT

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00860434

SECURE DOMAIN NAME SERVICE

AMELIORATIONS APORTEES A UN PROTOCOLE DE RESEAU AGILE POUR SECURISER LES COMMUNICATIONS A DISPONIBILITE DE SYSTEME ASSUREE

Patent Applicant/Patent Assignee:

- SCIENCE APPLICATIONS INTERNATIONAL CORPORATION

10260 Campus Point Drive, MS #F3, San Diego, CA 92121; US; US(Residence); US(Nationality); (For all designated states except: US)

Patent Applicant/Inventor:

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12026 Lisa Marie Court, Fairfax, VA 22033; US; US(Residence); US(Nationality)

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38710 Goose Creek Lane, Leesburg, VA 20175; US; US(Residence); US(Nationality)

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Legal Representative:

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Banner & Witcoff, Ltd., 1001 G Street, N.W., Eleventh Floor, Washington, DC 20001-4597; US:

	Country	Number	Kind	Date
Patent	WO	200192997	A2-A3	20011206
Application	WO	2001US13260		20010425
Priorities	US	2000558210		20000426

Designated States: (Protection type is "Patent" unless otherwise stated - for applications prior to 2004)

AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG,
BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ,
DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE,
GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,
KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU,
LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO,
NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK,
SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ,
VN, YU, ZA, ZW

[EP] AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;
GR; IE; IT; LU; MC; NL; PT; SE; TR;
[OA] BF; BJ; CF; CG; CI; CM; GA; GN; GW; ML;
MR; NE; SN; TD; TG;
[AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;
UG; ZW;
[EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

Publication Language: English
Filing Language: English
Fulltext word count: 33021
Detailed Description:

...server side replies by sending an information packet to the client computer that has been modified at the kernel layer to containing virtual private connection information in the payload portion of the reply information packet. Preferably, the information ...

7/3K/5 (Item 2 from file: 349) [Links](#)
Fulltext available through: [Order File History](#)

PCT FULLTEXT

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00853885

PROTOCOL FOR SECURE COMMUNICATIONS
AMELIORATIONS PORTANT SUR UN PROTOCOLE DE RESEAU AGILE DESTINE A LA COMMUNICATION
PROTEGEE AVEC DISPONIBILITE DU SYSTEME GARANTIE

Patent Applicant/Patent Assignee:

- SCIENCE APPLICATIONS INTERNATIONAL CORPORATION
10260 Campus Point Drive, MS#F3, San Diego, CA 92121; US; US(Residence); US(Nationality); (For all designated states except: US)

Patent Applicant/Inventor:

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12026 Lisa Marie Court, Fairfax, VA 22033; US; US(Residence); US(Nationality)
- SHORT Robert Durham III
38710 Goose Creek Lane, Leesburg, VA 20175; US; US(Residence); US(Nationality)
- MUNGER Edmund Colby
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- SCHMIDT Douglas Charles
230 Oak Court, Severna Park, MD 21146; US; US(Residence); US(Nationality)
- WILLIAMSON Michael
26203 Ocala Circle, South Riding, VA 20152; US; US(Residence); US(Nationality)
Legal Representative:

● CURTIN Joseph P(et al)(agent)

Banner & Witcoff, Ltd., 1001 G Street, N.W., Eleventh Floor, Washington, DC 20001-4597; US;

	Country	Number	Kind	Date
Patent	WO	200186911	A2-A3	20011115
Application	WO	2001US13261		20010425
Priorities	US	2000558209		20000426

Designated States: (Protection type is "Patent" unless otherwise stated - for applications prior to 2004)

AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG,
BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ,
DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE,
GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,
KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU,
LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO,
NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK,
SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ,
VN, YU, ZA, ZW

[EP] AT, BE, CH, CY, DE, DK, ES, FI, FR, GB;
GR, IE, IT, LU, MC, NL, PT, SE, TR;

[OA] BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML;
MR, NE, SN, TD, TG;

[AP] GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ;
UG, ZW;

[EA] AM, AZ, BY, KG, KZ, MD, RU, TJ, TM;

Publication Language: English

Filing Language: English

Fulltext word count: 37053

Detailed Description:

...server side replies by sending an information packet to the client computer that has been modified at the kernel layer to containing virtual private connection information in the payload portion of the reply information packet.

Preferably, the information...

7/3K/6 (Item 3 from file: 349) [Links](#)Fulltext available through: [Order File History](#)

PCT FULLTEXT

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00846329

PARALLEL COMPRESSION/DECOMPRESSION SYSTEM AND METHOD FOR IMPLEMENTATION OF IN-MEMORY
COMPRESSED CACHE
SYSTEME DE COMPRESSION/DECOMPRESSION EN PARALLELE ET PROCEDE DE MISE EN OEUVRE DE CACHE
COMPRESSE EN MEMOIRE

Patent Applicant/Patent Assignee:

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Inventor(s):

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Legal Representative:

● HOOD Jeffrey C(agent)

Conley, Rose & Tayon, P.C., P.O. Box 398, Austin, TX 78767-0398; US;

	Country	Number	Kind	Date
Patent	WO	200180016	A2-A3	20011025
Application	WO	2001US12408		20010416
Priorities	US	2000550380		20000414

Designated States: (Protection type is "Patent" unless otherwise stated - for applications prior to 2004)

AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG,
BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE,
DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH,

GM, HR, HU, ID, IL, IN, IS, JP, KE, KG,
 KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV,
 MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ,
 PL, PT, RO, RU, SD, SE, SG, SI, SK, SL,
 TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU,
 ZA, ZW
 [EP] AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;
 GR; IE; IT; LU; MC; NL; PT; SE; TR;
 [OA] BF; BJ; CF; CG; CI; CM; GA; GN; GW; ML;
 MR; NE; SN; TD; TG;
 [AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;
 UG; ZW;
 [EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

Publication Language: English
 Filing Language: English
 Fulltext word count: 49923

Detailed Description:
 ...data transfer.

The user virtual buffer addresses specified in the incoming VO request are preferably translated into a kernel virtual address that the driver can use. In one embodiment, the Flags field in the Device.....used.

In embodiments where buffered I/O is used, the buffer address may already be a kernel virtual address that points to non-paged system-space. Consequently, the driver may not have to...

7/3K/7 (Item 4 from file: 349) [Links](#)

Fulltext available through: [Order File History](#)

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00758780

MEMORY MANAGEMENT IN DISTRIBUTED COMPUTER SYSTEM
 GESTION DE MEMOIRE DANS UN SYSTEME INFORMATIQUE REPARTI

Patent Applicant/Patent Assignee:

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Intellectual Property Administration, P.O. Box 272400, 3404 East Harmony Road, M/S 35, Fort Collins, CO 80527-2400;
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● IBM CORPORATION

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 US; US(Residence); US(Nationality); (For all designated states except: US)

● COMPAQ COMPUTER CORP

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 US; US(Residence); US(Nationality); (For all designated states except: US)

● ADAPTEC INC

Intellectual Property Administration, P.O. Box 272400, 3404 East Harmony Road, M/S 35, Fort Collins, CO 80527-2400;
 US; US(Residence); US(Nationality); (For all designated states except: US)

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● BENNER Alan F

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● GARCIA David J

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● JOHN RECIO Renato

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Legal Representative:

● BILLIG Patrick

Hewlett-Packard Company, Intellectual Property Administration, P.O. Box 272400, 3404 E. Harmony Road, M/S 35, Fort Collins, CO 80527-2400; US;

	Country	Number	Kind	Date
Patent	WO	200072170	A1	20001130
Application	WO	2000US14282		20000524
Priorities	US	99135664		19990524
	US	99154150		19990915

Designated States: (Protection type is "Patent" unless otherwise stated - for applications prior to 2004)

AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR,
 BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM,
 EE, ES, FI, GB, GD, GE, GH, GM, HU, ID,
 IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
 LK, LR, LS, LT, LU, LV, MA, MD, MG, MK,
 MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU,
 SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT,
 TZ, UA, UG, US, UZ, VN, YU, ZA, ZW
 [EP] AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;
 GR; IE; IT; LU; MC; NL; PT; SE;
 [OA] BF; BJ; CF; CG; CI; CM; GA; GN; GW; ML;
 MR; NE; SN; TD; TG;
 [AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;
 UG; ZW;
 [EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

Publication Language: English

Filing Language: English

Fulltext word count: 13647

Detailed Description:

...to map a virtual address into a physical address, because many 1 5 operating system kernel programs use virtual addresses instead of effective addresses for I/O operations.

For example, a disk device driver... embodiment, the operating system kernel processes, such as a disk device driver, can use a kernel call to translate the virtual address to a physical address and then pass the physical address to SANIC...

7/3K/8 (Item 5 from file: 349) [Links](#)

Fulltext available through: [Order File History](#)

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00576314

RECOVERABLE METHODS AND SYSTEMS FOR PROCESSING INPUT/OUTPUT REQUESTS INCLUDING VIRTUAL MEMORY ADDRESSES

PROCEDES ET SYSTEMES RECUPERABLES DE TRAITEMENT DE DEMANDES D'ENTREE/SORTIE CONTENANT DES ADRESSES DE MEMOIRE VIRTUELLE

Patent Applicant/Patent Assignee:

- MICROSOFT CORPORATION

Inventor(s):

- FORIN Alessandro

	Country	Number	Kind	Date
Patent	WO	200039687	A1	20000706
Application	WO	99US30859		19991228
Priorities	US	98222696		19981229

Designated States: (Protection type is "Patent" unless otherwise stated - for applications prior to 2004)

AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR,
 BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM,
 EE, ES, FI, GB, GD, GE, GH, GM, HR, HU,
 ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ,
 LC, LK, LR, LS, LT, LU, LV, MA, MD, MG,
 MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU,
 SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT,
 TZ, UA, UG, UZ, VN, YU, ZA, ZW, GH, GM,
 KE, LS, MW, SD, SL, SZ, TZ, UG, ZW, AM,
 AZ, BY, KG, KZ, MD, RU, TJ, TM, AT, BE,
 CH, CY, DE, DK, ES, FI, FR, GB, GR, IE,
 IT, LU, MC, NL, PT, SE, BF, BJ, CF, CG,
 CI, CM, GA, GN, GW, ML, MR, NE, SN, TD,
 TG

Publication Language: English

Filing Language: English

Fulltext word count: 13526

Detailed Description:

...registration may include transmitting a memory registration request to the VI kernel agent. The VI kernel agent translates the virtual memory address of the descriptor to a physical memory address utilizing the host page tables...

7/3K/9 (Item 6 from file: 349) [Links](#)

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00387760

METHOD AND SYSTEM FOR IMPLEMENTING DATA TRANSFERS

PROCEDE ET SYSTEME DE REALISATION DE TRANSFERTS DE DONNEES

Patent Applicant/Patent Assignee:

- THE TRUSTEES OF PRINCETON UNIVERSITY

Inventor(s):

- BLUMRICH Matthias A

- FELTEN Edward W

- LI Kai

- DUBNICKI Cezary

	Country	Number	Kind	Date
Patent	WO	9728503	A1	19970807
Application	WO	97US1108		19970127
Priorities	US	96595792		19960202

Designated States: (Protection type is "Patent" unless otherwise stated - for applications prior to 2004)

AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY,
CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI,
GB, GE, HU, IL, IS, JP, KE, KG, KP, KR,
KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG,
MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU,
SD, SE, SG, SI, SK, TJ, TM, TR, TT, UA,
UG, UZ, VN, KE, LS, MW, SD, SZ, UG, AM,
AZ, BY, KG, KZ, MD, RU, TJ, TM, AT, BE,
CH, DE, DK, ES, FI, FR, GB, GR, IE, IT,
LU, MC, NL, PT, SE, BF, BJ, CF, CG, CI,
CM, GA, GN, ML, MR, NE, SN, TD, TG

Publication Language: English

Filing Language:

Fulltext word count: 14454

Detailed Description:

...virtual memory to serve as the source or destination of the data transfer;
2. The kernel translates the virtual addresses to physical addresses, verifies the user-level process's permission to perform the requested... ..2.
3* The DMA controller 2 performs the requested data transfer, and then notifies the kernel by changing a status register or causing an interrupt.
4* The kernel detects that the data transfer...

?

